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02/25/02

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10080683	02/25/2002	257	797	2811	Owens

**APPLICANTS: Tomita Kazuo; Ueno Atsushi;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CON OF 09/484,066 01/18/2000 PAT 6,376,924

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** FOREIGN APPLICATIONS VERIFIED:

JAPAN P11-206305 07/21/1999

PG-PUB DO NOT PUBLISH ☐ RESOUND ☐
 Foreign priority claimed ☐ yes ☐ no
 35 USC 119 conditions met ☐ yes ☐ no
 Verified and acknowledged Examiners's initials
 TITLE: Semiconductor device with an input terminal and a signal line and a method of fabricating the same

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAMER		Primary Examiner Application Examiner	
<input type="checkbox"/> PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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